BIOS Setup Manual

SR124-2A/SR224-2A/
SR121-2A/SR221-2A

Revision : 1.4
Release Date : 2025/10/02

Device Control

This is a history of the documents highlighting significant changes per revision number and date.

Revision	Description	Date
0.1	Initial release	2024/05/16
0.2	Add description note below: - Main section - Security section - Power Button Disabled - AC Loss Control Remove item: - SEV-TIO Support	2024/06/27
1.0	Added product support list of front page.	2024/11/11

1.1	Modify below section:
	- Section1.2.1
	  - TPM20 to 2.0 string
	  - Remove Disable Block Sid item
	- Section 1.2.3 of Authentication option
	- Section 1.2.6 for remove warning.
	- Section 1.2.7 IPv4 HTTP default
	- Section 1.2.10 of DF Cstate option
	- Section 1.4.1 of system mode
	- Section 1.4.2
	  - Factory Key Provision default
	  - Default Key contents of Secure Boot variable
		2024/11/25

1.2	- Section1.2.1 for remove Disable Block Sid item
	- Section1.2.4 for add UMC Common Options.
	- Section1.2.4.2.1 for modify order option on memory interleaving
	- Section1.4:
	  - Add Secure Boot list
	  - Add Section1.4.3
		2024/11/28

1.3	- Section1.2.10 add description of option for Turin only	2025/1/3
1.4	Update model name	2025/10/02

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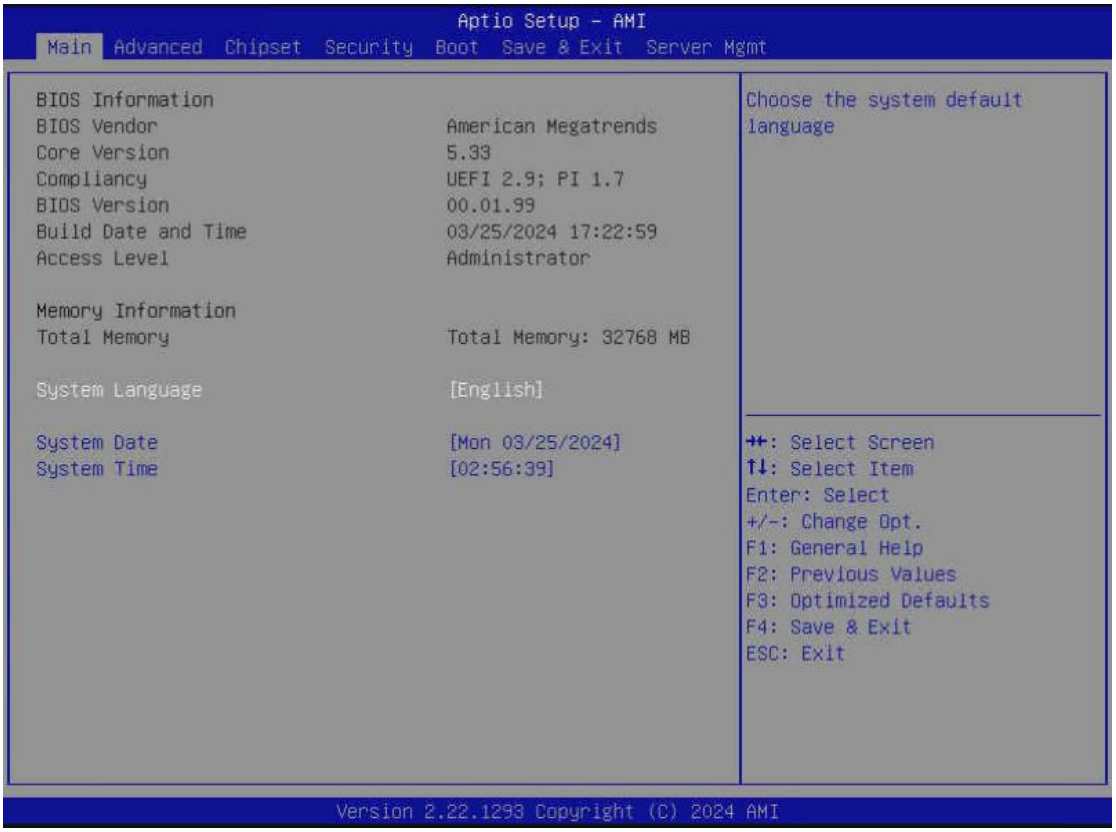
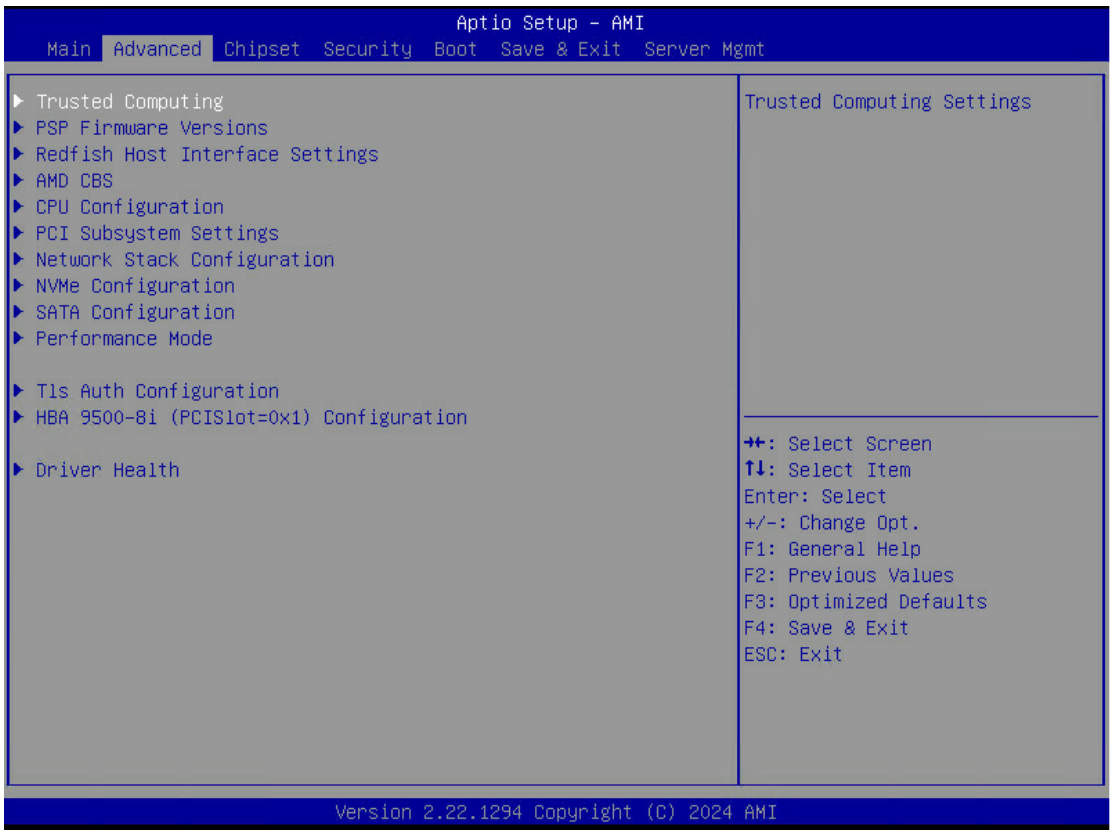
1. Setup menu

1.1. Main



Note: System Date and Time will be reset by removed battery.

1.2. Advanced

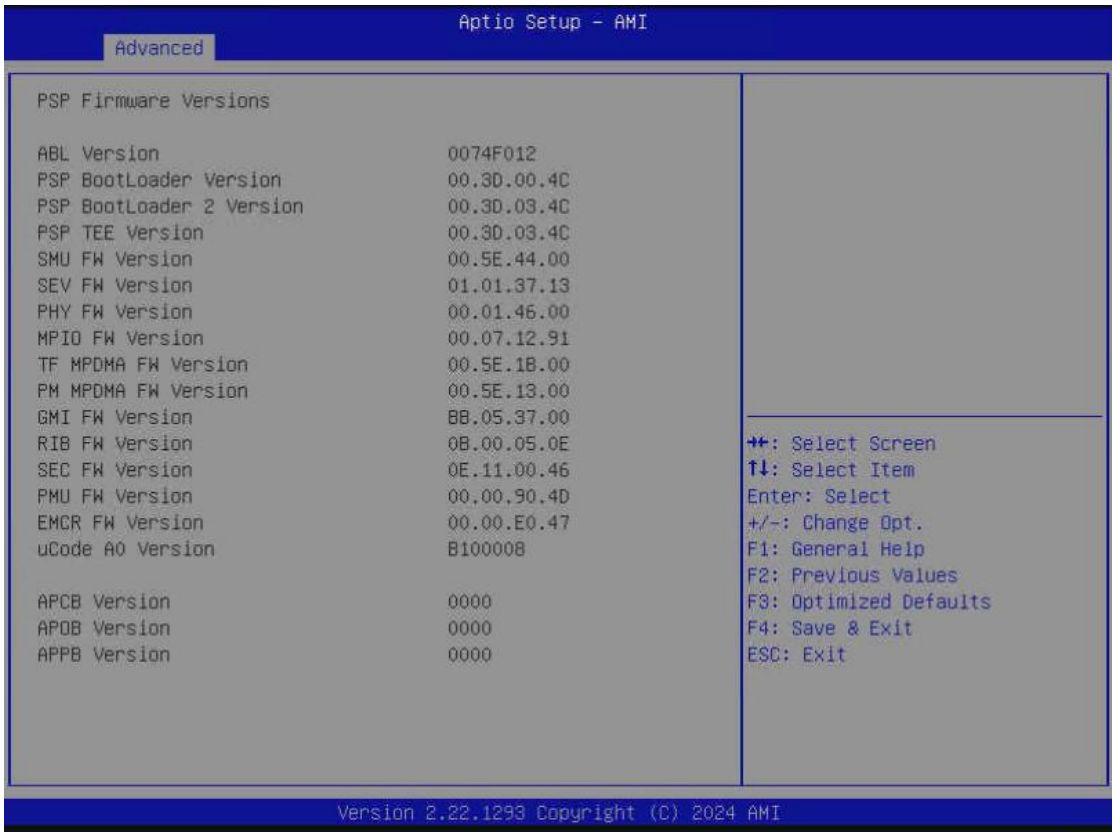


1.2.1 Trusted Computing

TPM 2.0 Device Found		
Item	**Option**	**Description**
Firmware Version:	x.x	
Vendor:	xxx	
Security Device Support	Disable/ Enable	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available
Active PCR banks	xxx	
Available PCR banks	xxx	

SHA256 PCR Bank	Disabled/ Enabled	Enable or Disable SHA256 PCR Bank
SHA384 PCR Bank	Disabled/ Enabled	Enable or Disable SHA384 PCR Bank
Pending operation	None/ TPM Clear	Schedule an Operation for the Security Device. NOTE: Your Computer will reboot during restart in order to change State of Security Device
Platform Hierarchy	Disabled/ Enabled	Enable or Disable Platform Hierarchy
Storage Hierarchy	Disabled/ Enabled	Enable or Disable Storage Hierarchy
Endorsement Hierarchy	Disabled/ Enabled	Enable or Disable Endorsement Hierarchy
Physical Presence Spec Version	1.2/ 1.3	Select to Tell O.S. to support PPI Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3.
TPM 2.0 Interface Type	TIS	Select the Communication Interface to TPM 20 Device.
PH Randomization	Disabled/ Enabled	Enables or Disables Platform Hierarchy randomization. DO NOT ENABLE THIS QUESTION IN PRODUCTION PLATFORMS. THIS IS FOR DEVELOPMENT TESTING. OVERRIDE ChangePlatformAuth ELINK for production platforms supporting TXT.
Device Select	TPM 1.2/ TPM 2.0/ Auto	TPM 1.2 will restrict support to TPM 1.2 devices, TPM 2.0 will restrict support to TPM 2.0 devices, Auto will support both with the default set to TPM 2.0 devices if not found, TPM 1.2 devices will be enumerated

1.2.2 PSP Firmware Versions



Note: Follow AMD AGESA shown.

1.2.3 Redfish Host Interface Settings

Redfish Host Interface Settings		
Item	**Option**	**Description**
Redfish	Disabled/ Enabled	Enable/Disable AMI Redfish
BMC Redfish Version	x.x.x	
BIOS Redfish Version	x.x.x	
Authentication mode	Authentication None/	Select authentication mode

	Basic Authentication /Session Authentication	
Redfish BMC Settings		
Item	**Option**	**Description**
IP address	xxx.xxx.xxx.xxx	
IP Mask address	xxx.xxx.xxx.xxx	
IP Port	xxx	

1.2.4 AMD CBS

AMD CBS		
Item	**Option**	**Description**
AMD CBS Revision Number	xxx	
CPU Common Options		CPU Common Options
DF Common Options		DF Common Options
UMC Common Options		UMC Common Options
NBIO Common Options		NBIO Common Options

1.2.4.1 CPU Common Options

CPU Common Options		
Item	**Option**	**Description**

CCD/Core/Thread Enablement		CCD/Core/Thread Enablement
SMT Control	Disabled/ Enable/ Auto	Can be used to disable symmetric multithreading. To re-enable SMT, a POWER CYCLE is needed after selecting the 'Enable' option. Select ‘Auto’ base on BIOS PCD.
Prefetcher settings		Prefetcher settings
Local APIC Mode	Compatibility/ xAPIC/ X2APIC/ Auto	Select local APIC operation modes.
SMEE	Disable/ Enable/ Auto	Control secure memory encryption enable Enabling both SMEE and SME-MK is not supported. Results in #GP.
SEV Control	Enable/ Disable	Can be used to disable SEV. To re-enable SEV, a POWER CYCLE is needed after selecting the "Enable" option. Show this item when SMEE = Enable.

1.2.4.1.1 CCD/Core/Thread Enablement

CCD/Core/Thread Enablement		
Item	**Option**	**Description**
CCD Control	Auto/ Depends on CPU	Sets the number of active CCDs. Once this option has been used to remove any CCDs, a POWER CYCLE is required in order for future selections to take effect.
Core Control	Auto/ Depends on CPU	Sets the number of cores to be used. Once this option has been used to remove any cores, a POWER CYCLE is required in order for future selections to take effect.

1.2.4.1.2 Prefetcher settings

Prefetcher settings		
Item	**Option**	**Description**
L1 Stream HW Prefetcher	Disable/ Enable/ Auto	Uses history of memory access patterns to fetch additional sequential lines in ascending or descending order

L1 Stride Prefetcher	Disable/ Enable/ Auto	Uses memory access history of individual instructions to fetch additional lines when each access is a constant distance from the previous.
L1 Region Prefetcher	Disable/ Enable/ Auto	Uses memory access history to fetch additional lines when the data access for a given instruction tends to be followed by other data accesses.
L2 Stream HW Prefetcher	Disable/ Enable/ Auto	Uses history of memory access patterns to fetch additional sequential lines in ascending or descending order
L2 Up/Down Prefetcher	Disable/ Enable/ Auto	Uses memory access history to determine whether to fetch the next or previous line for all memory accesses.
L1 Burst Prefetch Mode	Disable/ Enable/ Auto	Option to Enable | Disable L1 Burst Prefetch Mode

1.2.4.2 DF Common Options

DF Common Options		
Item	**Option**	**Description**
Memory Addressing		Memory Addressing
Link		Link

1.2.4.2.1 Memory Addressing

Memory Addressing		
Item	**Option**	**Description**
NUMA nodes per socket	NPS0/ NPS1/ NPS2/ NPS4/ Auto	Specifies the number of desired NUMA nodes per socket. Zero will attempt to interleave the two sockets together.
Memory interleaving	Disabled/ Enabled/ Auto	Allows for disabling memory interleaving. Note that NUMA nodes per socket will be honored regardless of this setting.

Location of private memory regions	Distributed/ Consolidated/ Consolidated to 1st DRAM pair/ Auto	Controls whether or not the private memory regions (PSP, SMU and CC6) are at the top of DRAM, at the top of 1st DRAM pair or distributed. Note that distributed requires memory on all dies. Note that it will always be at the top of DRAM if some dies do not have memory regardless of this option's setting. Also, Consolidation to 1st DRAM pair is only valid in the non-interleaved case.

1.2.4.2.2 Link

Link		
Item	**Option**	**Description**
xGMI Link Configuration	3 xGMI Links/ 4 xGMI Links	Show Only

1.2.4.3 UMC Common Options

UMC Common Options		
Item	**Option**	**Description**
DDR Security		DDR Security

1.2.4.3.1 DDR Security

DDR Security		
Item	**Option**	**Description**
TSME	Auto/ Enabled/ Disabled	Transparent SME

1.2.4.4 NBIO Common Options

NBIO Common Options

Item	Option	Description
SMU Common Options		SMU Common Options
IOMMU/ Security		IOMMU/Security

1.2.4.4.1 SMU Common Options

SMU Common Options		
Item	**Option**	**Description**
TDP Control	Manual/ Auto	Auto = Use the fused TDP Manual = User can set customized TDP
TDP	0	TDP [W] (in decimal) Show this item when TDP Control = Manual.
PPT Control	Manual/ Auto	Auto = Use the fused PPT Manual = User can set customized PPT
PPT	0	PPT [W] (in decimal) Show this item when PPT Control = Manual.

1.2.4.4.2 IOMMU/Security

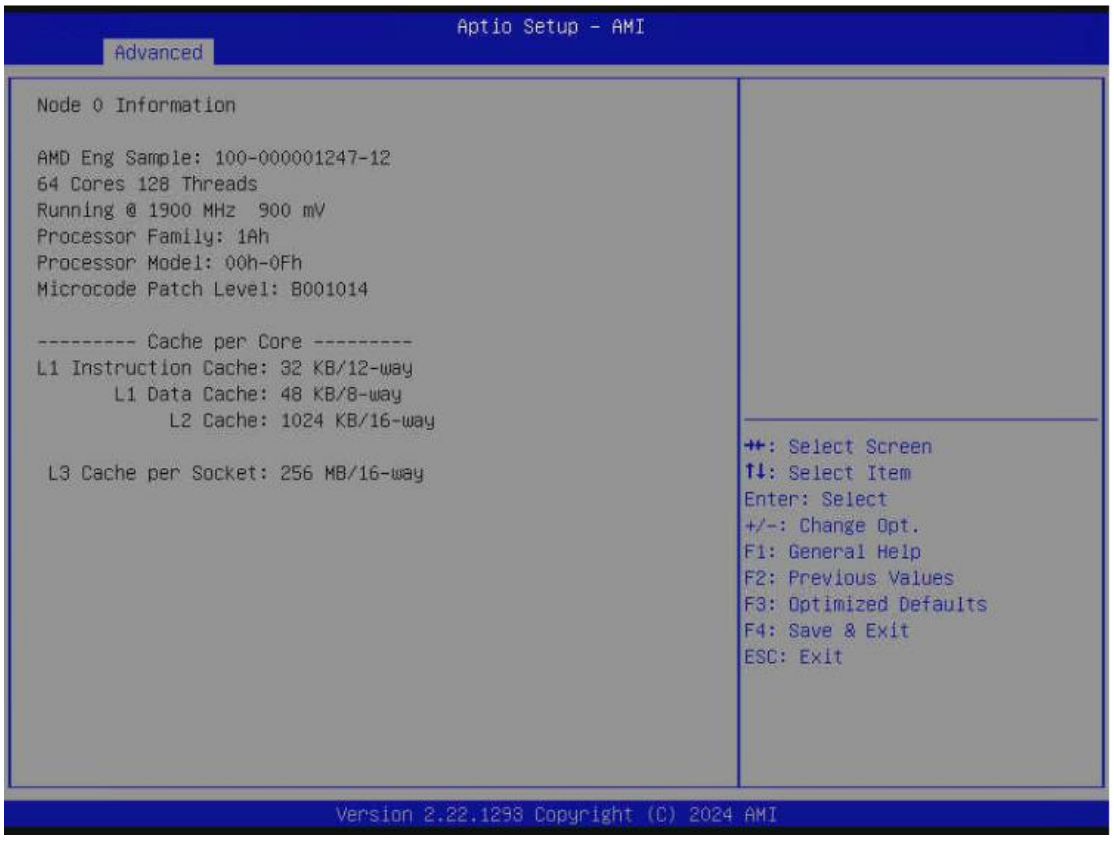
IOMMU/Security		
Item	**Option**	**Description**
SEV-SNP upport	Disable/ Enable/ Auto	Enables support for Secure Encrypted Virtualization and Secure Nested Paging
DRTM Memory Reservation	Disabled/ Enabled/ Auto	Reserve 128MB memory below Bottom IO for DRTM. It is required to be enabled for Secured-Core Server function.
DRTM Virtual Device Support	Disabled/ Enabled/ Auto	Enable DRTM ACPI virtual device.
DMA Protection	Auto/ Enabled/ Disabled	Enable DMA remap support in IVRS IVinfo Field.

IOMMU	Disabled/ Enabled/ Auto	Enable/Disable IOMMU
DMAr Support	Disabled/ Enabled/ Auto	Enable DMAr system protection during POST.

1.2.5 CPU Configuration

CPU Configuration		
Item	**Option**	**Description**
SVM Mode	Disabled/ Enabled	Enable/disable CPU Virtualization
Node x Information (x depends on your platform)		View Information related to Node x

1.2.5.1 Node x Information



1.2.6 PCI Subsystem Settings

PCI Settings Common for all Devices:		
Item	**Option**	**Description**
Above 4G Decoding	Enabled	Globally Enables or Disables 64bit capable Devices to be Decoded in Above 4G Address Space (Only if System Supports 64 bit PCI Decoding).
SR-IOV Support	Disabled/ Enabled	If system has SR-IOV capable PCIe Devices, this option Enables or Disables Single Root IO Virtualization Support.
BME DMA Mitigation	Disabled/ Enabled	Re-enable Bus Master Attribute disabled during Pci enumeration for PCI Bridges after SMM Locked

1.2.7 Network Stack Configuration

Item	Option	Description
Network Stack	Disabled/ Enabled	Enable/Disable UEFI Network Stack
IPv4 PXE Support	Disabled/ Enabled	Enable/Disable IPv4 PXE boot support. If disabled, IPv4 PXE boot support will not be available.
IPv4 HTTP Support	Disabled/ Enabled	Enable/Disable IPv4 HTTP boot support. If disabled, IPv4 HTTP boot support will not be available.
IPv6 PXE Support	Disabled/ Enabled	Enable/Disable IPv6 PXE boot support. If disabled, IPv6 PXE boot support will not be available.
IPv6 HTTP Support	Disabled/ Enabled	Enable/Disable IPv6 HTTP boot support. If disabled, IPv6 HTTP boot support will not be available.
PXE boot wait time	0	Wait time in seconds to press ESC key to abort the PXE boot. Use either +/- or numeric keys to set the value.
Media detect count	1	Number of times the presence of media will be checked. Use either +/- or numeric keys to set the value.

1.2.8 NVMe Configuration

Item	Option	Description
NVMe Configuration		NVMe Device Options Settings.

No NVMe Device Found /NVMe Device name		

1.2.9 SATA Configuration

Item	Option	Description
SATA Configuration		SATA Devices Information.
SATA Controller	(S:XX B:XX D:XX F:XX)	
Port X	XX	Display SATA device, when SATA device connected on SATA controller. (X depends on your platform)

1.2.10 Performance Mode

Item	Option	Description
Performance Mode	Performance/ Balance/ Custom	Performance: Keep in higher frequency without power saving. Balance: Keep in lower frequency and power when OS idle, and increase frequency and power by loading.
Power Profile Selection	High Performance Mode/ Efficiency Mode/ Maximum IO Performance Mode/ Balanced Memory Performance Mode/ Balanced Core Performance Mode/ Balanced Core Memory Performance Mode	0 = High Performance Mode; 1 = Efficiency Mode; 2 = Maximum IO Performance Mode; 3 = Balanced Memory Performance Mode; 4 = Balanced Core Performance Mode;(Turin only) 5 = Balanced Core Memory Performance Mode; (Turin only)
Determinism Enable	Power/ Performance	0 = Power; 1 = Performance
ACPI SRAT L3 Cache As NUMA Domain	Disabled/ Enabled/ Auto	Enabled: Each CCX in the system will be declared as a separate NUMA domain.

		Disabled: Memory Addressing \ NUMA nodes per socket will be declared.
ACPI CST C2 Latency	100	Enter in microseconds (decimal value). Larger C2 latency values will reduce the number of C2 transitions and reduce C2 residency. Fewer transitions can help when performance is sensitive to the latency of C2 entry and exit. Higher residency can improve performance by allowing higher frequency boost and reduce idle core power. With Linux kernel 6.0 or later, the C2 transition cost is significantly reduced. The best value will be dependent on kernel version, use case, and workload.
DF Cstate	Disabled/ Enabled/ Auto	Enable = Enable the feature : Disable = Disable the feature

NOTES: To achieve maximum energy efficiency rating, Enable efficiency mode will take actions like as lower bus speed, cap core/iod frequency to lower overall power consumption.

The xGMI bandwidth will be capped to lower than standard speed.

1.2.11 TLS Auth Configuration

Item	Option	Description
Server CA Configuration		Press <Enter> to configure Server CA.
Client Cert Configuration		Client cert configuration is unsupported currently.

1.2.11.1 Server CA Configuration

Item	Option	Description
Enroll Cert		Press <Enter> to enroll cert.
Delete Cert		Press <Enter> to delete cert.

1.2.11.1.1 Enroll Cert

Item	Option	Description
Enroll Cert Using File		Enroll Cert Using File

Cert GUID		Input digit character in 11111111-2222-3333-4444-1234567890ab format.
Commit Changes and Exit		Commit Changes and Exit
Discard Changes and Exit		Discard Changes and Exit

1.2.11.1.2 Delete Cert

Item	Option	Description
xxxxxxxx-xxxx-xxxx-xxxx-xxxxxxxxxxx	Disabled/ Enabled	GUID for CERT

1.2.12 Driver Health

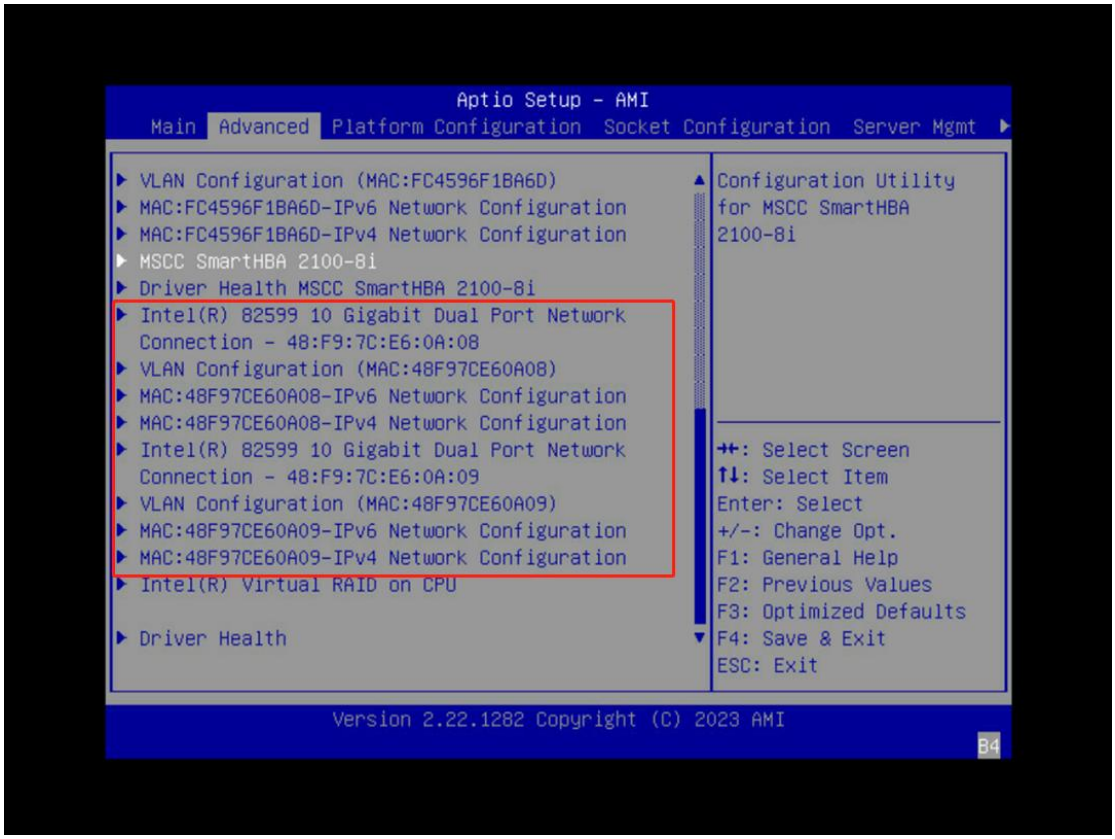
Driver Health		Provides Health Status for the Drivers/Controllers

1.2.13 Network and RAID Card

When one or more PCI devices are added on system, the corresponding item is displayed on the advanced page and screen is shown below.

Example 1:

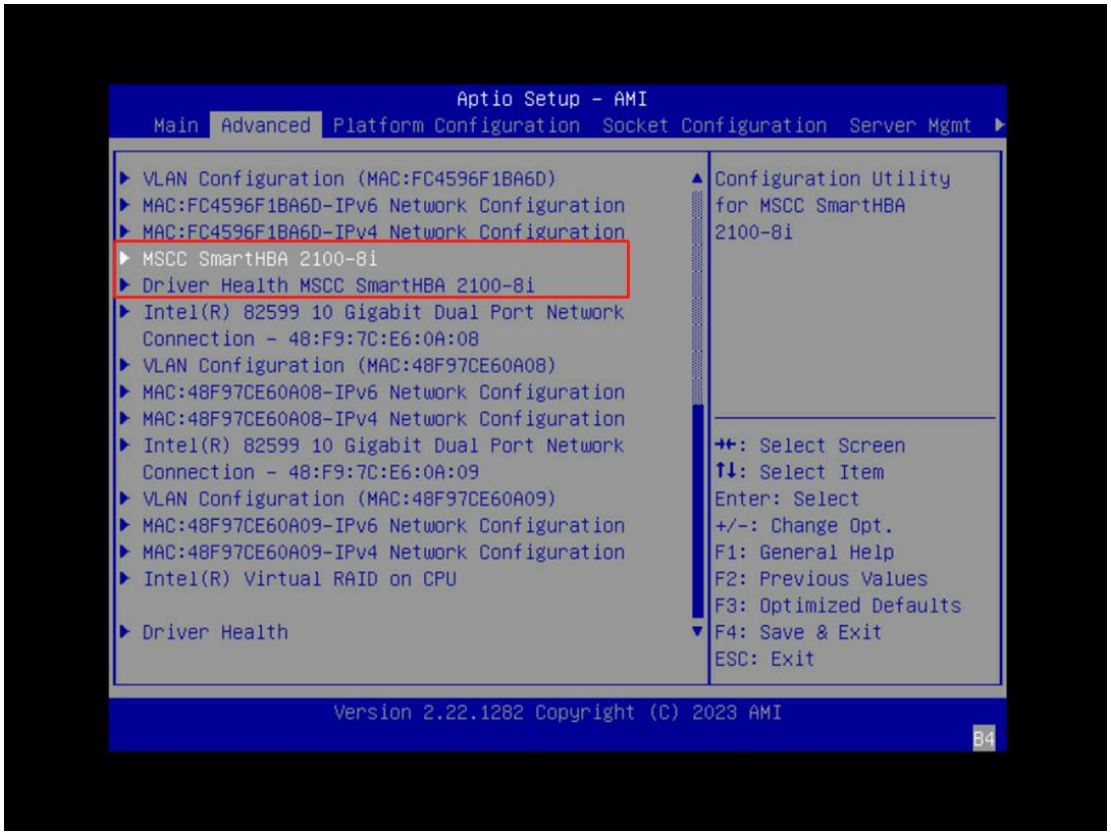
Network card: Intel(R) 82599 10 Gigabit Dual Port Network Connection



NOTES: These cards are reference only. Please consult Device SPEC to see the details.

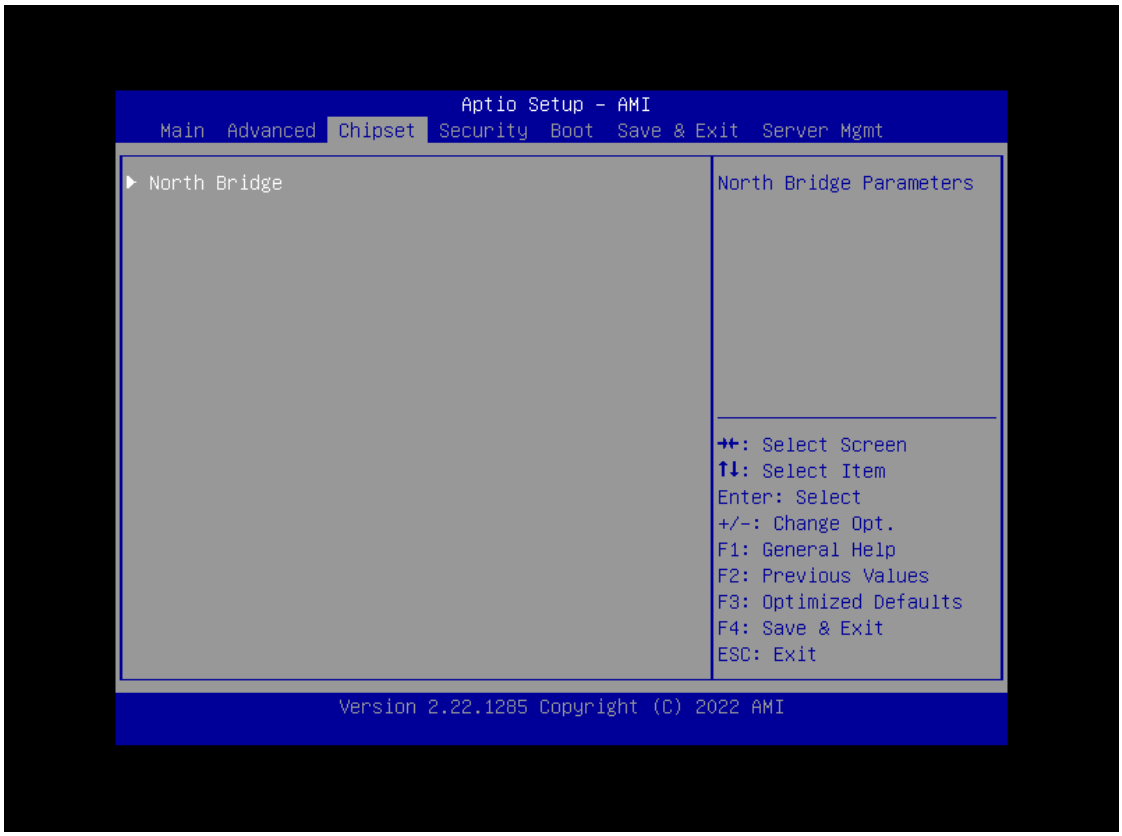
Example 2 :

RAID card: MSCC SmartHBA 2100-8i



NOTES: These cards are reference only. Please consult Device SPEC to see the details.

1.3. Chipset



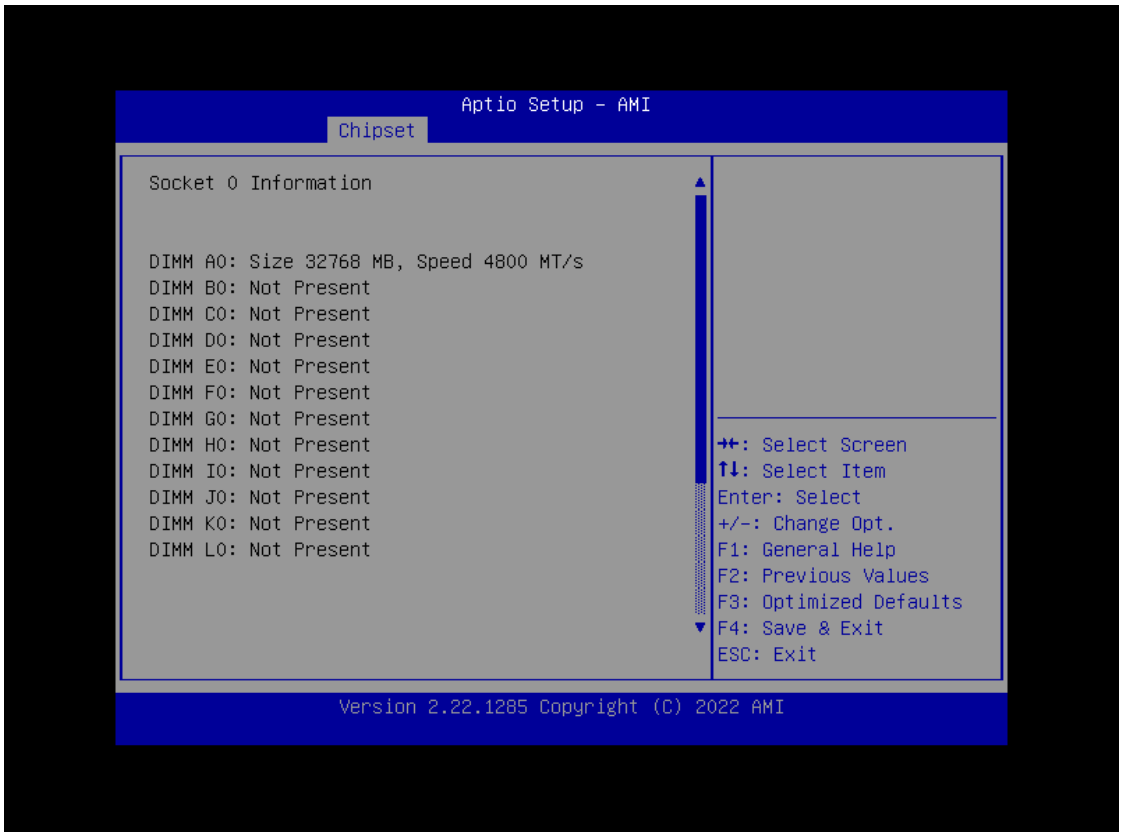
Item	Option	Description
North Bridge		North Bridge Parameters

1.3.1 North Bridge

North Bridge Configuration		
Memory Information		
Total Memory: xxxxxxx MB		
Socket x Information		View Information related to Socket x.

1.3.1.1 Socket x Information

Socket x Information		View Information related to Socket x.


Aptio Setup – AMI
Chipset
Socket 0 Information
DIMM A0: Size 32768 MB, Speed 4800 MT/s
DIMM B0: Not Present
DIMM C0: Not Present
DIMM D0: Not Present
DIMM E0: Not Present
DIMM F0: Not Present
DIMM G0: Not Present
DIMM H0: Not Present
DIMM I0: Not Present
DIMM J0: Not Present
DIMM K0: Not Present
DIMM L0: Not Present
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.22.1285 Copyright (C) 2022 AMI

1.4. Security

Item	Option	Description
Administrator Password		Set Administrator Password
User Password		Set User Password
Secure Boot		Secure Boot configuration

Note: No effect by clear CMOS and load default.

1.4.1 Secure Boot

Secure Boot		
Item	**Option**	**Description**
System mode		Shown status only
Secure Boot	Disabled/ Enabled	Secure Boot feature is Active if Secure Boot is Enabled,Platform Key(PK) is enrolled and the System is in User mode.The mode change requires platform reset

	Not Active	
Secure Boot Mode	Standard/ Custom	Secure Boot mode options:Standard or Custom.In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication
Restore Factory Keys		Force System to User Mode. Install factory default Secure Boot key databases
Reset to Setup Mode		Delete all Secure Boot key databases from NVRAM
Enter Audit Mode		Enter Audit Mode workflow. Transitions from User to Audit Mode will result in erasing of PK variable
Enter Deployed Mode		Transition between Deployment and User Modes
Expert Key Management		Enables expert users to modify Secure Boot Policy variables without variable authentication

Note: No effect by clear CMOS.

1.4.2 Expert Key Management

Key Management		
Item	**Option**	**Description**
Vendor Keys	Valid	Shown status only
Factory Key Provision	Disabled/ Enabled	Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode
Restore Factory Keys		Force System to User Mode. Install factory default Secure Boot key databases
Reset to Setup Mode		Delete all Secure Boot key databases from NVRAM
Enroll Efi Image		Allow the image to run in Secure Boot mode.Enroll SHA256 Hash certificate of a PE image into Authorized Signature Database (db)
Export Secure Boot variables		Save NVRAM content of Secure Boot variable to a file

Secure Boot variable	Size	Keys	Key Source	
Platform Key (PK)	825	1	Factory	Enroll Factory Defaults or load certificates from a file: 1.Public Key Certificate: a)EFI_SIGNATURE_LIST b)EFI_CERT_X509 (DER) c)EFI_CERT_RSA2048 (bin) d)EFI_CERT_SHAXXX 2.Authenticated UEFI Variable 3.EFI PE/COFF Image(SHA256) Key Source: Factory,Modified,Mixed Note: This contents are dependency of Key included
Key Exchange Keys (KEK)	3066	2	Factory	
Authorized Signatures (db)	6133	4	Factory	
Forbidden Signatures(dbx)	17836	371	Factory	
Authorized TimeStamps(dbt)	0	0	No Keys	
OsRecovery Signatures(dbr)	0	0	No Keys	

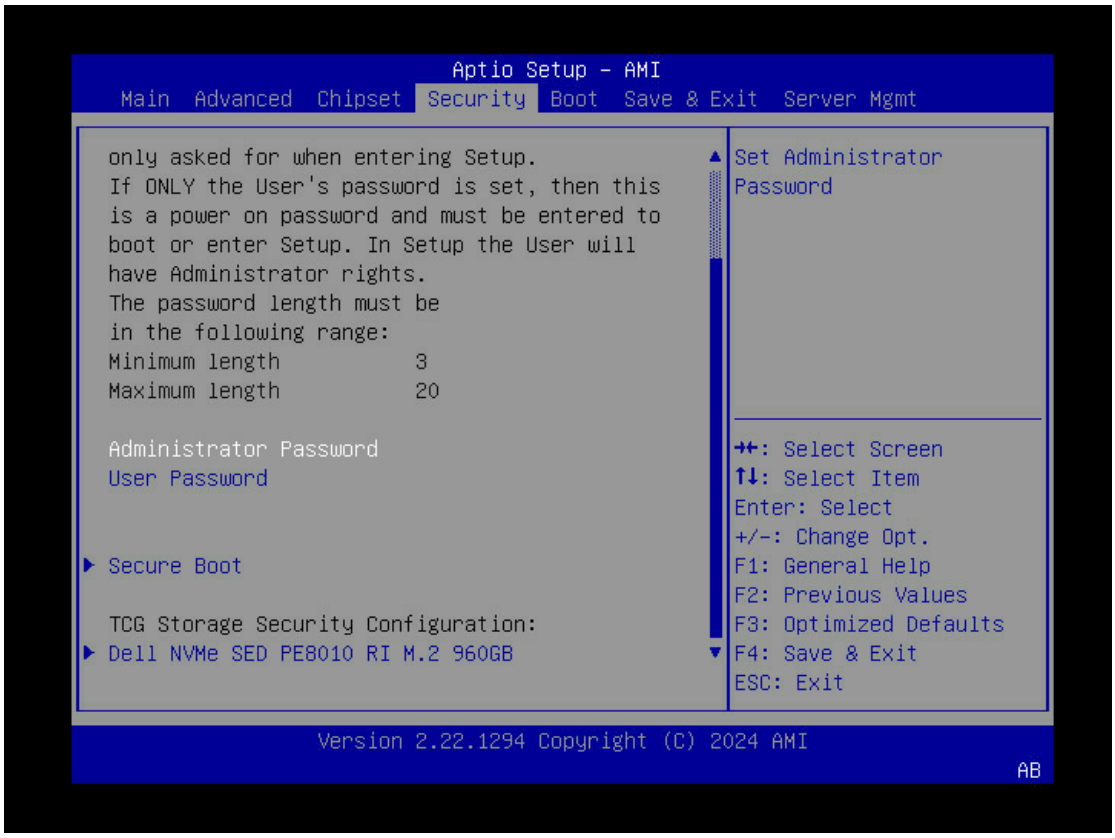
Note: No effect by clear CMOS.

1.4.3 TCG Storage Security Configuration

When one or more storages which support security feature are added on system, the corresponding item is displayed on the Security page and screen is shown below.

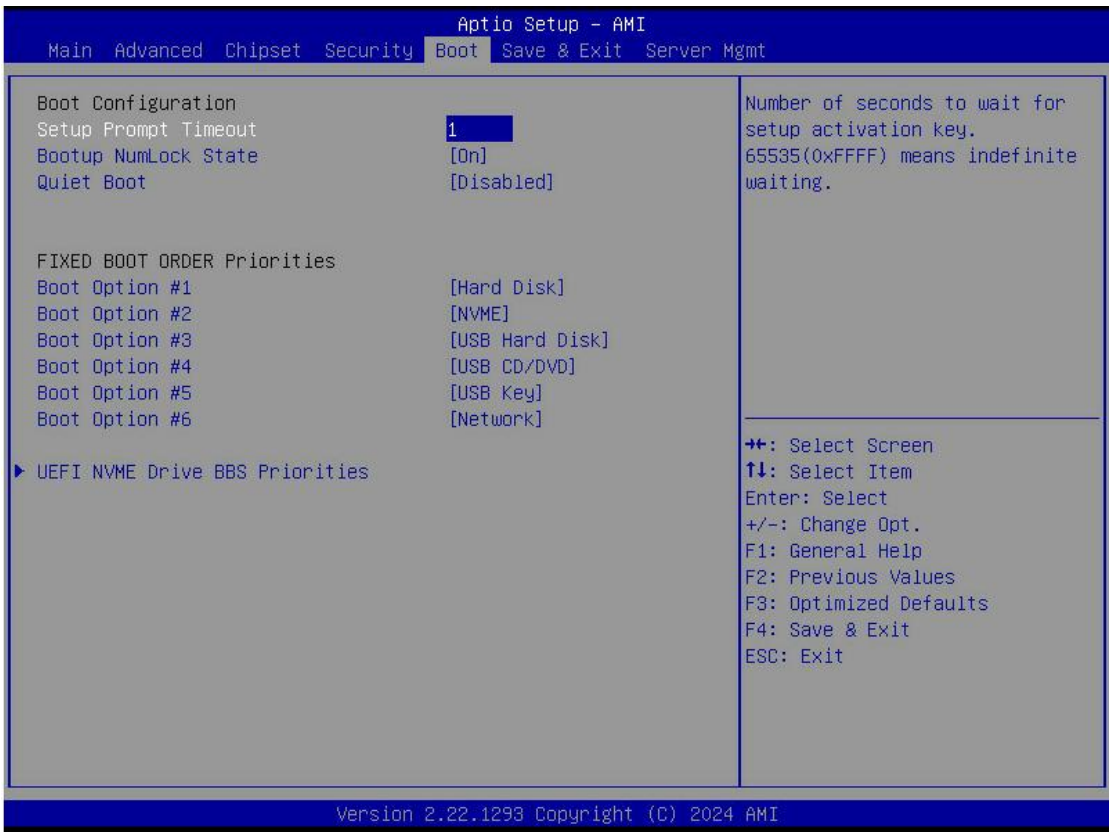
Example:

Dell NVMe SED PE8010 RI M.2 960GB



NOTES: Reference only. Please consult Storage SPEC to see the details.

1.5. Boot



Boot Configuration		
Item	**Option**	**Description**
Setup Prompt Timeout	1	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting
Bootup NumLock State	On/ Off	Select the keyboard NumLock state
Quiet Boot	Disabled/ Enabled	Enables or disables Quiet Boot option Note: No effect by clear CMOS.
FIXED BOOT ORDER Priorities		
Boot Option #X		Sets the system boot order

UEFI NVME Drive BBS Priorities		
Item	**Option**	**Description**

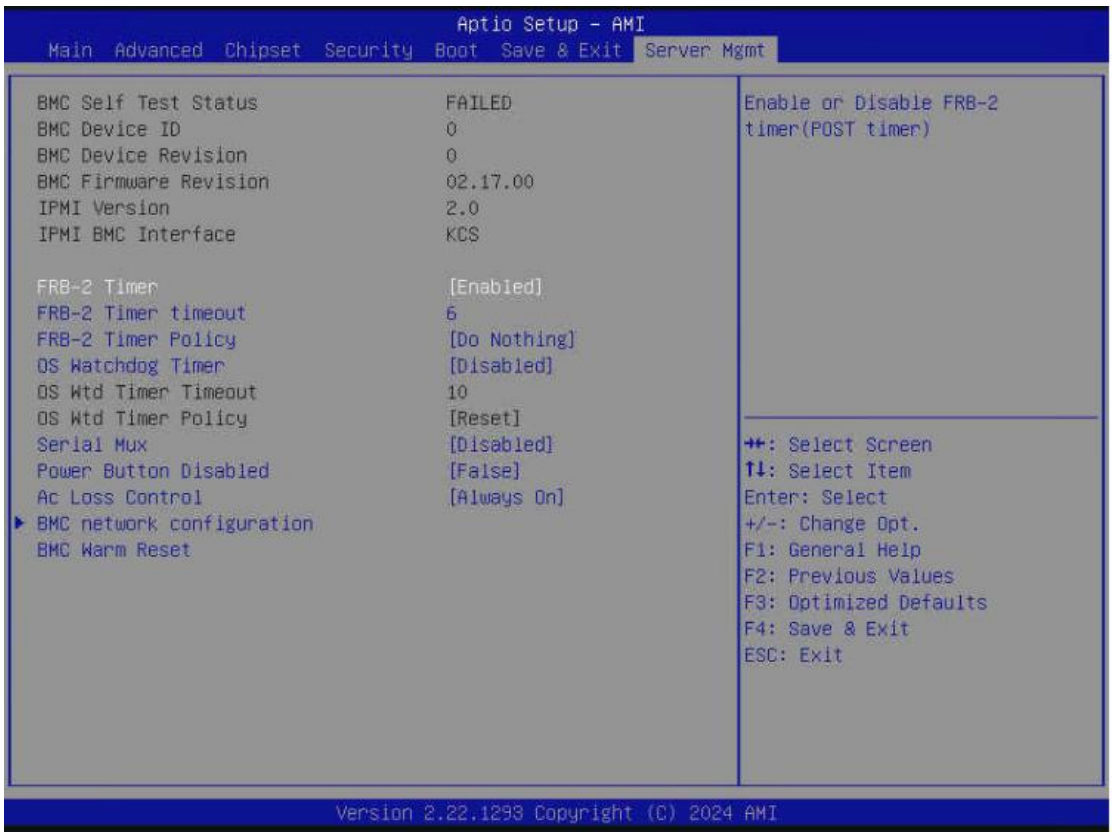
Boot Option #X	[Product name] / Disable	Specifies the Boot Device Priority sequence from available UEFI NVME Drives.
UEFI USB Key Drive BBS Priorities		
Boot Option #X	[UEFI: Product name] / Disable	Specifies the Boot Device Priority sequence from available UEFI USB Key Drives.
UEFI NETWORK Drive BBS Prorities		
Boot Option #X	[UEFI: PXE IPv4 In...] / [UEFI: PXE IPv4 In...] / [UEFI: PXE IPv4 In...] / [UEFI: PXE IPv4 In...] / Disable	Specifies the Boot Device Priority sequence from available UEFI NETWORK Drives.

1.6. Save & Exit

Save Options		
Item	**Option**	**Description**
Save Changes and Reset		Reset the system after saving the changes.
Discard Changes and Reset		Reset system setup without saving any changes.
Save Changes		Save Changes done so far to any of the setup options.
Discard Changes		Discard Changes done so far to any of the setup options.

Default Options		
Restore Defaults		Restore/Load Default values for all the setup options.
Save as User Defaults		Save the changes done so far as User Defaults.
Restore User Defaults		Restore the User Defaults to all the setup options.
Boot Override		
UEFI: Built-in EFI Shell		

1.7. Server Mgmt



Item	Option	Description
BMC Self Test Status	for example, PASSED	Information depends on your platform
BMC Device ID	for example, XX	Information depends on your platform
BMC Device Revision	for example, XX	Information depends on your platform
BMC Firmware Revision	for example, XX.XX.XX	Information depends on your platform
IPMI Version	for example, X.X	Information depends on your platform
IPMI BMC Interface	for example, KCS, USB	Information depends on your platform

FRB-2 Timer	Enabled/ Disabled	Enable or Disable FRB-2 timer(POST timer)
FRB-2 Timer timeout	6	Enter value Between 1 to 30 min for FRB-2 Timer Expiration
FRB-2 Timer Policy	Do Nothing/ Reset/ Power Down/ Power Cycle	Configure how the system should respond if the FRB-2 Timer expires. Not available if FRB-2 Timer is disabled.
OS Watchdog Timer	Enabled/ Disabled	If enabled, starts a BIOS timer which can only be shut off by Management Software after the OS loads. Helps determine that the OS successfully loaded or follows the OS Boot Watchdog Timer policy.
OS Wtd Timer Timeout	10	Enter the value Between 1 to 30 min for OS Boot Watchdog Timer Expiration. Not available if OS Boot Watchdog Timer is disabled.
OS Wtd Timer Policy	Do Nothing/ Reset/ Power Down/ Power Cycle	Configure how the system should respond if the OS Boot Watchdog Timer expires. Not available if OS Boot Watchdog Timer is disabled.
Serial Mux	Enabled/ Disabled	Press <Enter> to enable or disable Serial Mux configuration.
Power Button Disabled *	True/ False	Select Power Button function behavior
AC Loss Control *	Always Off/ Always On/ Last state	Select Ac Loss Control Method

Note *: The Option is synchronized from BMC.

1.7.1 BMC Network Configuration

Configure IPv4 support		
Shared / Dedicated LAN		
Item	**Option**	**Description**
Configuration Address source	Unspecified/ Static/ DynamicBmcDhcp/ DynamicBmcNonDhcp	Select to configure LAN channel parameters statically or dynamically(by BIOS or BMC). Unspecified option will not modify any BMC network parameters during BIOS phase
Current Configuration Address source	DynamicAddressBmcDhcp	
Station IP address	XXX.XXX.XXX.XXX	Enter station IP address
Subnet mask	XXX.XXX.XXX.XXX	Enter subnet mask
Station MAC address	XX-XX-XX-XX-XX-XX	Station MAC address from BMC
Router IP address	XXX.XXX.XXX.XXX	Enter router IP address
Router MAC address	XX-XX-XX-XX-XX-XX	Enter router MAC address
Configure IPv6 support		
Shared / Dedicated LAN		
IPv6 Support	Enabled/ Disabled	Enable or Disable IPv6

Configuration Address source	Unspecified/ Static/ DynamicBmcDhcp	Select to configure LAN channel parameters statically or dynamically(by BIOS or BMC). Unspecified option will not modify any BMC network parameters during BIOS phase
Current Configuration Address source	DynamicAddressBmcDhcp	Current LAN Configuration statically or dynamically(by BIOS or BMC). Unspecified for not Configured address space
Station IPv6 address	:::::::	Enter station IPv6 address
Prefix Length	*Setting by DHCP server*	Change the prefix length
IPv6 address status	Active	Status of station IPv6 address to BMC.
IPv6 DHCP Algorithm	DHCPv6	Providing the DHCP method used like DHCPv6 or StateLess Address Auto Configuration(SLAAC).
Configuration Router Lan1 Address source	Unspecified/ Static/ DynamicBmcDhcp	Select to configure LAN channel parameters statically or dynamically(by BIOS or BMC). Unspecified option will not modify any BMC network parameters during BIOS phase
Current Router Configuration Address source	DynamicAddressBmcDhcp	Current IPv6 Router LAN Configuration statically or dynamically by BMC). Unspecified for not Configured address space
IPv6 Router IP Address	:::::::	Change the IPv6 Router IP Address
IPv6 Router Prefix Length	*Setting by DHCP server*	Change the IPv6 Router Prefix Length
IPv6 Router Prefix Value	:::::::	Change the IPv6 Router Prefix Value

1.7.2 BMC Warm Reset

BMC Warm Reset		Press <Enter> to do Warm Reset BMC.